

Class	Subclass
ISSUE CLASSIFICATION	

PATENT NUMBER

## U.S. UTILITY Patent Application

**O.I.P.E.**

PATENT DATE

SCANNED 24 O.I.P.E. WA Q.A.

APPLICATION NO. 09/891310	CONT/PRIOR F	CLASS 327 714	SUBCLASS 727	ART UNIT 2816	EXAMINER BRITT
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## APPLICANTS

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RSU

**TITLE**

Integrated circuit device having boundary scan register

PTO-2040  
12/99[illegible]

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
				<b>ISSUE FEE</b>  Amount Due      Date Paid	
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<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Legal Instruments Examiner) (Date)				
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